

"Express Mail" Mailing Label Number: EL581517927US

Date of Deposit: November 16, 2000

JC860 U.S. PTO



11/16/00

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 on the date indicated above and is addressed to the Assistant Commissioner For Patents, Washington, D.C. 20231

By: Benjamin Trella

JC860 U.S. PTO
09/7/13940



Attorney's Docket No. XER 2 0359
D/A0540

UTILITY PATENT APPLICATION TRANSMITTAL LETTER

(Only for nonprovisional applications under 37 CFR 1.53(b))

Assistant Commissioner for Patents
Washington, D.C. 20231

Transmitted herewith for filing in the patent application of:

Zhigang Fan and Shen-Ge Wang

Entitled: **METHOD AND APPARATUS FOR RENDERING A CONTINUOUS TONE IMAGE REPRESENTATIVE WITH MULTIPLE DIGITAL HALFTONING TECHNIQUES**

Enclosed are:

- ☒ 11 sheets of specification, 5 sheets of claims, 1 sheet of Abstract.
☒ 4 sheets of drawings, Figures 1-4.
☒ an assignment of an invention to XEROX CORPORATION (and transmittal therefor).
☒ a Declaration and Power of Attorney for Patent Application.
☒ Information Disclosure Statement and PTO-1449

CLAIMS AS FILED

FOR	NO. FILED	NO. EXTRA	RATE	FEE
TOTAL CLAIMS	20 - 20 =	0	[XX] \$18 LARGE [] \$09 SMALL	\$ 710.00
INDEPENDENT CLAIMS	3 - 3 =	0	[XX] \$80 LARGE [] \$40 SMALL	\$ 0.00
BASIC FEE	[XX] LARGE ENTITY	\$710	[] SMALL ENTITY \$355	
TOTAL FILING FEE				\$ 710.00

☒ Please charge Deposit Account No. 24-0037 in the amount of \$ 710.00.

☒ The Commissioner is hereby authorized to charge any additional fees which may be required at any time during the prosecution of this application without specific authorization, or credit any overpayment to Deposit Account No. 24-0037.

Date

11/16/00

Attorney of Record

R. Scott Speroff

Reg. No. 37,450

FAY, SHARPE, FAGAN,

MINNICH & McKEE, LLP

1100 Superior Avenue, Seventh Floor

Cleveland, Ohio 44114-2518

(216) 861-5582

METHOD AND APPARATUS FOR RENDERING A CONTINUOUS TONE IMAGE
REPRESENTATION WITH MULTIPLE DIGITAL HALFTONING TECHNIQUES

Background of the Invention

The present invention relates to the digital imaging art. It finds particular application in conjunction with a method and apparatus for rendering a continuous tone (a.k.a. 5 contone) image representation with multiple digital halftoning techniques, and will be described with particular reference thereto.

In the digital reproduction of documents, an image is conveniently represented as a bitmap, which may be described as 10 an electronic image with discrete signals (hereinafter, pixels) defined by position and density. In such a system, density is described as one level in a number of possible states or levels. When more than two levels of density are used in the description of the image, the levels are often termed "gray", indicating 15 that they vary between a maximum and minimum, and without reference to their actual color.

Most printing systems have the ability to reproduce an image with a small number of levels, most commonly two, although other numbers are possible. Common input devices 20 including document scanners, digital cameras and the computer imagery generators, however, are capable of describing an image with a substantially larger number of gray levels, with 256 levels a commonly selected number, although larger and smaller levels are possible. It is required that an image initially 25 described at a large set of levels also be describable at a smaller set of levels, in a manner which captures the intent of the user.

09713940-11600

For color images, a plurality of bitmaps, each forming a color separation are combined. Each color separation may be defined by a number of gray levels in excess of the capability of the printer. In digital reproduction of color documents, each of the color separations is reduced from the input number of levels to a smaller output number of levels. The multiple color separations are combined together at printing to yield the final color print. Commonly, color documents are formed using cyan, magenta and yellow colorants or cyan, magenta, yellow and black colorants. A larger number or alternative colorants may also be used.

In printing digital image representations of a contone image, the desired density over an area is commonly achieved by halftoning, where image density variation is represented by placing greater or less numbers of ON pixels in a discrete area of the image. One digital halftoning technique for reproducing a contone image representation is error diffusion. Error diffusion attempts to maintain gray by making the conversion from gray pixels to binary or other level pixels on a pixel-by-pixel basis. That is, each pixel is examined with respect to a threshold, and the difference between the gray level pixel value and the output value is forwarded to a selected group or set of neighboring pixels, in accordance with a weighting scheme. Error diffusion usually produces halftones with decent overall image quality. However, it also introduces artifacts such as "worms" and periodic patterns. The worm artifacts depend on input values, and generally occur at the highlight and shadow regions. The periodic patterns are most severe when the input values happen to be a simple fraction of the output range, e.g. $\frac{1}{2} \times 255$, $\frac{1}{3} \times 255$, $\frac{2}{3} \times 255$, etc.

Another digital halftoning technique for reproducing a contone image representation is stochastic screening. In U.S. Patent No. 5,673,121 by Shen-Ge Wang (a co-inventor of the

present application), an idealized stochastic screen is characterized by having all of the predominant color dots (black or white) uniformly distributed. A process is described to approach this optimization by iteratively selecting pairs of
5 threshold levels in the screen matrix, and measuring the approach to the idealized stochastic screen. The threshold values are then swapped in position to determine whether the swap improves the measurement or not. If it does, the swap is maintained. The process is iterated until the desired result
10 is obtained. Stochastic screening typically creates excellent halftones at the highlights and shadows. It can also avoid periodic patterns. However, stochastic screened halftones have relatively higher overall noise levels and lack the inherent edge enhancement typically provided by error diffusion.

15 Accordingly, it is considered desirable to develop a new and improved method and apparatus for rendering a continuous tone image representation with multiple digital halftoning techniques, that meets the above-stated needs and overcomes the foregoing difficulties and others while providing better and
20 more advantageous results.

Summary of the Invention

In accordance with one aspect of the present invention, a system for rendering an image representation is disclosed. The system includes a halftone processor; a printer
25 interfaced to the halftone processor; and a software routine operative on the halftone processor for a) classifying an input pixel value in either a first classification or a second classification, b) determining an output pixel value by error diffusing the input pixel value when the input pixel value is
30 classified in the first classification, c) determining a temporary output pixel value by stochastic screening the input

00713940-11600

pixel value when the input pixel value is classified in the second classification, d) determining an adjusted input pixel value based on the input pixel value, the temporary output pixel value and a weighting value, e) determining the output pixel value by error diffusing the adjusted input pixel value, and f) rendering the output pixel value in the printer.

In accordance with another aspect of the present invention, a method for rendering an image representation is disclosed. The method includes a) classifying an input pixel value in either a first classification or a second classification; b) determining an output pixel value by error diffusing the input pixel value when the input pixel value is classified in the first classification; c) determining a temporary output pixel value by stochastic screening the input pixel value when the input pixel value is classified in the second classification; d) determining an adjusted input pixel value based on the input pixel value, the temporary output pixel value and a weighting value; e) determining the output pixel value by error diffusing the adjusted input pixel value; and f) rendering the output pixel value.

In accordance with yet another aspect of the present invention, a digital halftoning system is disclosed. The system includes a processor; a marking device interfaced to the processor; and instructions operative on the processor for generating an output pixel value to be rendered by the marking device, the output pixel value being generated by a stochastic screening routine, an error diffusion routine, or a selected combination of the stochastic screening routine and the error diffusion routine depending on an input pixel value.

An advantage of the present invention is the provision of a method and apparatus for halftoning a digital image representation that combines error diffusion and stochastic screening to provide better output image quality.

009713940-111500

Still further advantages of the present invention will become apparent to those of ordinary skill in the art upon reading and understanding the following detailed description of the preferred embodiments.

5

Brief Description of the Drawings

The invention may take form in various components and arrangements of components, and in various steps and arrangements of steps. The drawings are only for purposes of illustrating a preferred embodiment and are not to be construed
10 as limiting the invention.

Figure 1 is a simplified block diagram of an exemplary halftoning system in which the present invention finds use;

Figure 2 is a simplified block diagram of an exemplary halftone processor associated with the halftoning system of
15 Figure 1;

Figure 3 is a block diagram of one separation processing circuit of the halftone processor of Figure 2 in accordance with the present invention; and

Figure 4 is an operational flowchart for rendering a
20 contone image representation in accordance with the present invention.

Detailed Description of the Preferred Embodiment

With reference now to Figure 1, a basic image processing or halftoning system A is shown. In the present
25 case, gray image data may be characterized as image signals, each pixel of which is defined at a single level or optical density in a set of (c) optical density levels (e.g. 256), the number of members in the set of levels being larger than desired. Each pixel will be processed in the manner described
30 hereinbelow, to redefine each pixel in terms of a new, smaller

set of (d) optical density levels (e.g. 2). In this process, (c) and (d) are integer values representing pixel depth, or a number of signal levels at which the pixel may appear. Thus, one common case of this method includes the conversion of data
5 from a relatively large set of gray levels to one of two legal or allowed binary levels for printing in a binary printer.

As used herein, the term "dot pattern" refers to a product or an image resulting from a screening process. A "screen cell", as used herein, refers to the set of pixels which
10 together will form the dot pattern, while the term "screen matrix" will be used to describe the set of values which together make up the set of threshold to be applied. A "pixel" refers to an image signal associated with a particular position in an image, having a density between white and black.
15 Accordingly, pixels are defined by intensity and position. A dot pattern is made up of a plurality of pixels. These terms are used for simplification and it should be understood that the appropriate sizing operations have to be performed for images where the input resolution in terms of scan pixels is different
20 from the output resolution in terms of print pixels.

The present invention can be employed in a black/white copier, such as Xerox 8830 Engineering Printer or in a typical color system where color documents are represented by multiple sets of image signals, each set (or separation) represented by
25 an independent channel, which is usually processed more or less independently. A "color image" as used herein is therefore a document including at least two separations, such as in the Xerox 4850 Highlight Color Printer, and commonly three or four separations, such as in the Xerox 4700 Color Laser Printer,
30 Xerox 5775 Digital Color Copier, or the Xerox 4900 printer, or sometimes more than four separations (a process sometimes called hi-fi color). One possible digital copier (a scanner/printer combination) is described for example, in U.S. Pat. No.

09773940-114500

5,014,123, incorporated herein by reference. Each separation provides a set of image signals which will drive a printer to produce one color of the image. In the case of multicolor printers, the separations superposed together form the color
5 image. In this context, pixels will be described as discrete image signals that represent optical density of the document image in a given small area thereof. The term "pixel" will be used to refer to such an image signal in each separation, as distinguished from "color pixel", which is the sum of the color
10 densities of corresponding pixels in each separation. The term "gray-scale", as used herein refers to image signals or values which can vary between maximum and minimum, irrespective of the color of the separation in which the signals are used.

With continued reference to Figure 1, an electronic
15 image representation of an original document (hereinafter, an image) from image input terminal such as scanner 10 derives electronic digital data in some manner, in a format related to the physical characteristics of the device, and commonly with pixels defined at (m) bits per pixel. Common color scanners,
20 such, for example, Xerox 5775 Digital Color Copiers, or the Pixelcraft 7650C, produce 8 bit/pixel data, at resolutions acceptable for many purposes. Since this is a color document, the image is defined with two or more separation bitmaps, usually with identical resolution and pixel depth. The
25 electronic image signals are directed through an image processing unit (IPU) 16 to be processed so that an image suitable for reproduction on an image output terminal (IOT), marking engine, or printer subsection 20, is obtained. Thus, the term "printer" as used throughout refers to a separate
30 printing device and to a subsystem of a conventional copier.

Image processing unit 16 commonly includes a halftone processor 18 which converts (m) bit digital image signals to (n) bit digital image signals, suitable for driving a particular

printer, where (m) and (n) are integer values. It also well within the contemplation of the present invention, to derive images electronically. In such cases, the images may be represented by a page description language (PDL) file, 5 describing the appearance of the page. In such a case, the IPU 16 might include processing element for decomposition of the page, and color conversions elements for providing appropriate signals for driving a printer.

Referring now to Figure 2, the color halftone 10 processor 18 uses four separations, $C(x,y)$, $M(x,y)$, $Y(x,y)$, $K(x,y)$, obtained and each processed independently for halftoning purposes, to reduce an m-bit input to an n-bit output. It will be appreciated that the invention is also applicable to the "single separation" or black and white reproduction situation 15 as well. A source of screen matrix information, such as screen matrix memory 106, provides one input to each separation processing circuit/routine 100, 102, 104, and 108 for each separation on a line 110, where the other input to each separation processing circuit/routine is an m-bit separation 20 bitmap pixel $i(x,y)$, where (i) represents the gray scale input value for the respective separation C, M, Y, or K. The output is an n-bit separation bitmap pixel $b(x,y)$, where (b) represents the binary output pixel value for the respective separation C, M', Y', or K', which can be directed to a printer 20. This 25 illustration is highly simplified, in that distinct screen matrices may be supplied to each separation processing circuit/routine.

Referring now to Figures 3 and 4, each separation of the electronic image representation is processed within a 30 respective one of the separation processing circuits/routines 100, 102, 104, 108 on a pixel-by-pixel basis in the same order as conventional error diffusion. The halftone rendering routine 150 starts by classifying an input pixel (x,y) into one of two

09713940.111500

classes, a stochastic screening class (S) and an error diffusion class (E), depending upon the gray scale value (i) of the input pixel being processed (x,y) (step and/or circuit 152). Gray scale values that belong to the (S) class include highlight and
 5 shadow regions. Gray scale values that cause period patterns (e.g. input values that happen to be a simple fraction of the output range, e.g. $\frac{1}{2} \times 255$, $\frac{1}{3} \times 255$, $\frac{2}{3} \times 255$, etc.) also belong to the (S) class. The remainder of the gray scale values belong to the (E) class. An exemplary classification function c(i) is
 10 given by:

	c(i) = E	for $8 \leq i < 78$, or
		$93 \leq i < 112$, or
		$143 < i \leq 162$, or
		$177 < i \leq 247$
15	c(i) = S	otherwise.

However, it is contemplated that any conventional pixel classification scheme can be used to determine whether the input pixel belongs to the (E) class or the (S) class.

If the current input pixel being processed (x,y),
 20 belongs to the (E) class (step and/or circuit 154), then a normal error diffusion procedure is applied to the current input pixel value i(x,y) (step and/or circuit 156) to generate an output pixel value b(x,y) that is rendered by the printer 20 in a conventional manner (step 158). Otherwise, a temporary output
 25 pixel value $b_t(x,y)$ is generated using a conventional stochastic screening algorithm (step and/or circuit 160). A suitable stochastic screening technique is disclosed in the previously-mentioned U.S. Patent No. 5,673,121 by Shen-Ge Wang, which is hereby incorporated by reference. Thus, the result of the
 30 stochastic screening step is to generate the temporary output pixel value $b_t(x,y)$ that is either ON (e.g. gray scale value

that is normalized to 255) or OFF (gray scale value that is normalized to 0). Thereafter, an adjusted input pixel value $i_a(x,y)$ is determined (step and/or circuit 162) from the following equation:

5
$$i_a(x,y) = i(x,y) + a[b_t(x,y) - i(x,y)] \quad \text{Eq. 1,}$$

where $i(x,y)$ is the input gray scale value (0-255) for the current input pixel being processed (x,y) , $b_t(x,y)$ is the temporary output pixel value generated by the stochastic screening step 160, and (a) is a weighting factor or value that
10 varies from 0 to 1 depending upon the value of $i(x,y)$ (step 164).

The adjustment to the input pixel value (i.e. $i_a(x,y)$) of Eq. 1 encourages error diffusion to generate the same output as the stochastic screening. The amount of encouragement
15 depends the factor (a) . That is, when $(a) = 1$, the output of the halftoning routine 150 will be the same as stochastic screening for most reasonably designed screens. When $(a) = 0$, the output of the halftoning routine 150 becomes ordinary error diffusion. However, when (a) is between 0 and 1, some
20 percentage of the stochastic screened pixel replaces the pixel before the error diffusion. The factor (a) is typically a function of $i(x,y)$. To prevent abrupt transitions (e.g. boundary effects) between adjacent gray levels, $a[i(x,y)]$ is preferably a smooth function. An exemplary function for $a(i)$,
25 where (i) is the gray scale value for the input pixel (x,y) , is given by:

009713940-111500

```
a(i) = 3/4                for i < 4;
a(i) = 1/8(8-i)           for 4 ≤ i < 8;
a(i) = 1/8[1-|i-85|/8]    for 78 ≤ i < 93;
a(i) = 3/8[1-(127-i)/16]  for 112 ≤ i < 128; and
5 a(i) = a(255-i)         for i > 128
```

Error diffusion is then performed to produce the output pixel value $b(x,y)$, using $i_a(x,y)$ as the input (step 166). Thereafter, the output pixel value $b(x,y)$ is rendered by the printer 20 in a conventional manner (step 158). Control advances to step 168 where it is determined if there are additional input pixels to be processed. If so, control returns to step 152 for additional input pixel halftoning in the manner previously described. When all input pixels have been rendered, the routine 150 ends at step 170.

15 The steps 156 and 166 are shown as being separate to facilitate an understanding of the present invention. However, it should be appreciated that steps 156 and 166 can be implemented as a single error diffusion routine, with either the input pixel value $i(x,y)$ being error diffused, or the adjusted
20 input pixel value $i_a(x,y)$ being error diffused.

The invention has been described with reference to the preferred embodiment(s). Obviously, modifications and alterations will occur to others upon reading and understanding the preceding detailed description. It is intended that the
25 invention be construed as including all such modifications and alterations insofar as they come within the scope of the appended claims or the equivalents thereof.

Having thus described the preferred embodiment, the invention is now claimed to be:

1. A system for rendering an image representation, the system comprising:

a halftone processor;
a printer interfaced to the halftone processor; and
5 software means operative on the halftone processor
for:

- a) classifying an input pixel value in either a first classification or a second classification,
- 10 b) determining an output pixel value by error diffusing the input pixel value when the input pixel value is classified in the first classification,
- c) determining a temporary output pixel value by stochastic screening the input pixel value when the input pixel value is classified in the second classification,
- 15 d) determining an adjusted input pixel value based on the input pixel value, the temporary output pixel value and a weighting value,
- e) determining the output pixel value by error diffusing the adjusted input pixel value, and
- 20 f) rendering the output pixel value in the printer.

2. The system of claim 1, wherein the weighting value is based on the input pixel value.

3. The system of claim 1, wherein the weighting value is a function of the input pixel value.

4. The system of claim 1, wherein the adjusted input pixel value $i_a(x,y)$ is determined from the equation:

$$i_a(x,y) = i(x,y) + a[b_t(x,y) - i(x,y)],$$

where $b_t(x,y)$ is the temporary output pixel value, and (a) is the weighting value.

5. The system of claim 1, wherein a) includes determining whether the input pixel value represents at least one of a highlight region and shadow region of the image representation.

6. The system of claim 1, wherein a) includes determining whether the input pixel value is a predetermined fraction of a predetermined value.

7. The system of claim 1, wherein a) includes determining whether the input pixel value is a predetermined fraction of 255.

8. A method for rendering an image representation, the method comprising:

a) classifying an input pixel value in either a first classification or a second classification;

5 b) determining an output pixel value by error diffusing the input pixel value when the input pixel value is classified in the first classification;

c) determining a temporary output pixel value by stochastic screening the input pixel value when the input pixel value is classified in the second classification;

10 d) determining an adjusted input pixel value based on the input pixel value, the temporary output pixel value and a weighting value;

- e) determining the output pixel value by error diffusing the adjusted input pixel value; and
- f) rendering the output pixel value.

9. The method of claim 8, wherein the weighting value is based on the input pixel value.

10. The method of claim 8, wherein the weighting value is a function of the input pixel value.

11. The method of claim 8, wherein the adjusted input pixel value $i_a(x,y)$ is determined from the equation:

$$i_a(x,y) = i(x,y) + a[b_t(x,y) - i(x,y)],$$

where $b_t(x,y)$ is the temporary output pixel value, and (a) is
5 the weighting value.

12. The method of claim 8, wherein step a) includes determining whether the input pixel value represents at least one of a highlight region and shadow region of the image representation.

13. The method of claim 8, wherein step a) includes determining whether the input pixel value is a predetermined fraction of a predetermined value.

14. The method of claim 1, wherein step a) includes determining whether the input pixel value is a predetermined fraction of 255.

15. The method of claim 1, further including:

- g) repeating steps a)- f) for each pixel of the image representation.

16. A digital halftoning system comprising:
a processor;
a marking device interfaced to the processor; and
software means operative on the processor for
5 generating an output pixel value to be rendered by the marking
device, the output pixel value being generated by a stochastic
screening routine, an error diffusion routine, or a selected
combination of the stochastic screening routine and the error
diffusion routine depending on an input pixel value.

17. The system of claim 16, wherein the output pixel
value is determined by the selected combination of the
stochastic screening routine and the error diffusion routine
when the input pixel value represents one of a highlight region
5 and a shadow region of an image representation being rendered,
and when the input pixel value is a predetermined fraction of
255.

18. The system of claim 16, wherein:
a temporary output pixel value is produced by the
stochastic screening routine,
an adjusted input pixel value is determined based on
5 the temporary output pixel value, and
the adjusted input pixel value is error diffused to
produce the output pixel value.

19. The system of claim 18, wherein the adjusted
input pixel value $i_a(x,y)$ is determined from the equation:
$$i_a(x,y) = i(x,y) + a[b_t(x,y) - i(x,y)],$$

where $b_t(x,y)$ is the temporary output pixel value, and (a) is a
5 weighting value.

00713640-111600

20. The system of claim 19, wherein the weighting value is a function of the input pixel value.

09713943.11600

METHOD AND APPARATUS FOR RENDERING A CONTINUOUS TONE IMAGE
REPRESENTATION WITH MULTIPLE DIGITAL HALFTONING TECHNIQUES

Abstract of the Disclosure

A system for rendering an image representation is disclosed. The system includes a halftone processor; a printer interfaced to the halftone processor; and a software routine
5 operative on the halftone processor for a) classifying an input pixel value in either a first classification or a second classification, b) determining an output pixel value by error diffusing the input pixel value when the input pixel value is classified in the first classification, c) determining a
10 temporary output pixel value by stochastic screening the input pixel value when the input pixel value is classified in the second classification, d) determining an adjusted input pixel value based on the input pixel value, the temporary output pixel value and a weighting value, e) determining the output pixel
15 value by error diffusing the adjusted input pixel value, and f) rendering the output pixel value in the printer.

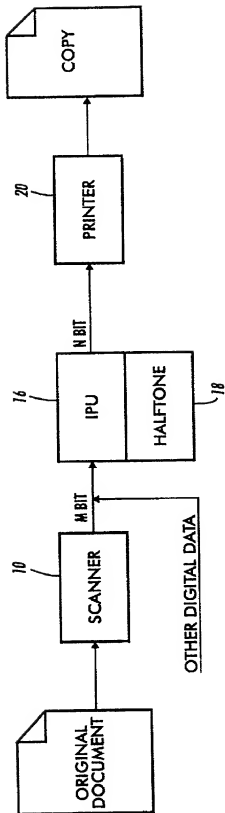


FIG. 1

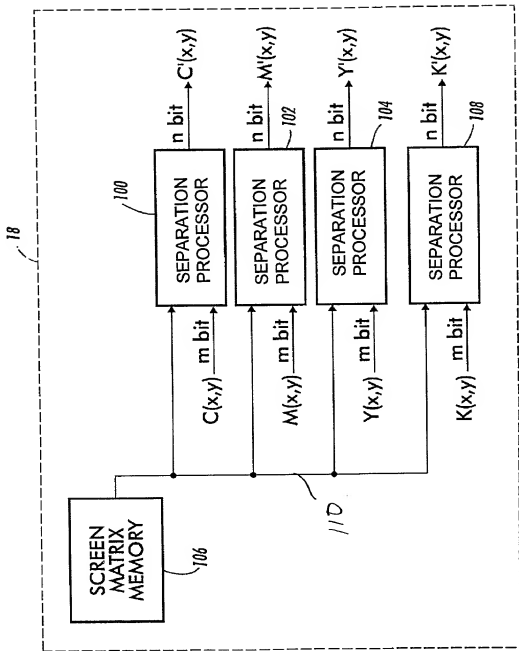


FIG. 2

100, 102, 104, 108

162

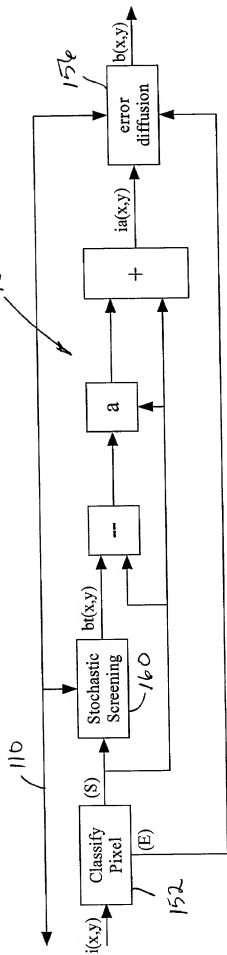
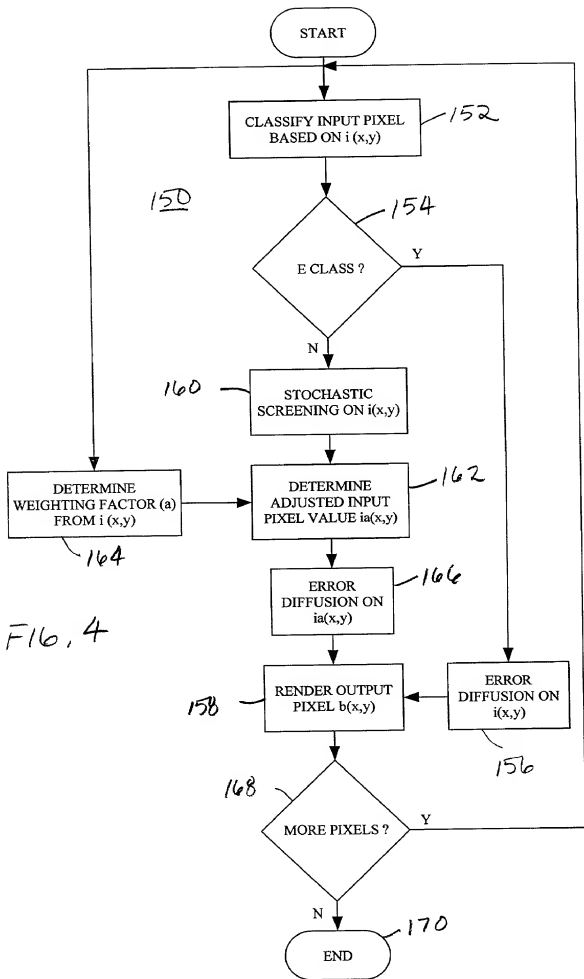


FIG. 3



Attorney's Docket No. XER 2 0359

D/A0540

DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As a below inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

METHOD AND APPARATUS FOR RENDERING CONTINUOUS TONE IMAGE REPRESENTATION WITH MULTIPLE DIGITAL HALFTONING TECHNIQUES

the specification of which

☒ is attached hereto _____ OR was filed on
Application Serial No.
and was amended on (if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119 or 365(b) of any foreign application(s) for patent or inventor's certificate, or 363(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below any foreign application(s) for patent or inventor's certificate or of any PCT international application having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s):

(Number)

(Country)

(Filing Date)

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:

Prior Provisional U.S. Patent Application(s):

(Application Serial No.)

(Filing Date)

I hereby claim the benefit under Title 35, United States, Section 120 of any United States application(s) or any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application or PCT international application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose material information which is material to patentability as defined in Title 37, of Federal Regulations Code, Section 1.56(a) which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Attorney's Docket No. XER 2 0359

D/A0540

Page 2 of 2

Prior U.S. Patent Application(s):

(Application Serial No.)

(Filing Date)

(Status)

(patented, pending, abandoned)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

Steven M. Asvill	Reg. No. 40,492	Mark E. Bandy	Reg. No. 35,788
Brian G. Benbenick	Reg. No. 41,463	John P. Cornely	Reg. No. 41,687
Joseph D. Dreher	Reg. No. 37,123	Christopher B. Fagan	Reg. No. 72,987
Jude A. Fry	Reg. No. 38,340	Steven M. Haas	Reg. No. 37,841
W. Scott Harders	Reg. No. 42,629	Michael E. Hudzinski	Reg. No. 34,185
Richard M. Klein	Reg. No. 33,000	Thomas E. Koozovsky, Jr.	Reg. No. 28,383
Sandra M. Koenig	Reg. No. 33,722	Brian E. Koudas	Reg. No. 40,685
Scott A. McCollister	Reg. No. 33,061	James W. McKee	Reg. No. 26,482
Richard J. Minnich	Reg. No. 24,175	Jay F. Moldenoway	Reg. No. 29,678
Philip J. Moy	Reg. No. 31,230	Timothy E. Neumann	Reg. No. 32,283
Patrick R. Reabe	Reg. No. 29,580	Albert P. Sharpe, III	Reg. No. 19,879
R. Scott Spreoff	Reg. No. 37,450	Mark S. Swat	Reg. No. 34,261
John E. Beck	Reg. No. 22,833	Richard B. Domingo	Reg. No. 36,784
Henry Fleischer	Reg. No. 25,582	Mark Costello	Reg. No. 31,342
Eugene O. Palazao	Reg. No. 20,881	Aditya Krishnan	Reg. No. 41,531

SEND CORRESPONDENCE TO:

Albert P. Sharpe, III, Esq.
Fay, Sharpe, Fagan,
Minnich & McKee, LLP
1100 Superior Avenue, 7th Floor
Cleveland, OH 44114-2518

DIRECT TELEPHONE CALLS TO:

(name and telephone number)

Albert P. Sharpe, III
(216) 861-5582

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both under Section 1001 of Title 18 of the United States code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Full name of sole or first joint inventor: ZHIGANG FAN

Inventor's Signature: Zhigang Fan

Residence: Webster, New York

Country of Citizenship: USA

Date: 11/15/2000

Post Office Address: 153 Yorktown Drive
Webster, NY 14580

Full name of second joint inventor: SHEN-GE WANG

Inventor's Signature: Sheng Wang

Residence: Fairport, New York

Country of Citizenship: USA

Date: 11/15/2000

Post Office Address: 9 Selborne Chase
Fairport, NY 14450